Altium Designer’s FPGA development environment can be used to capture, synthesize, place and route and download a digital system design into an FPGA. Place and route, the process of implementing the design on the target silicon, requires an intimate understanding of the functionality and architecture of the device, a task best performed by software tools provided by the device vendor. The vendor software is operated by the Altium Designer environment, which automatically manages all project and file handling aspects required to generate the FPGA program file. There is a large degree of user-control over this process.

This topic provides an advanced Altera designer with information on how to control the Altera place and route software options and properties, and also includes information on libraries.

Altera tools are integrated and accessed in the Altium Designer environment through the Devices view (View » Devices View). This view allows step-by-step control over the entire FPGA design process, enabling you to program and debug your system design on the FPGA.

Users wishing to change any of the default settings for the Altera tools should refer to Altera's _Introduction to Quartus II Manual. This document is available from the Altera website.

If you are not familiar with the Altera tools it is recommended that you start designing with the built-in default settings.

Supported Altera Architectures

Altera Place and Route Tools Configuration

Altera Quartus II Synthesizer Configuration

See Also

Processing the Captured FPGA Design

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